

IN THE CLAIMS

1 (Previously Presented). A method comprising:
accessing a configuration space on a platform integrated component;
detecting an add-in card for a platform, said add-in card intended to operate with said integrated component to implement wake packet filtering;
comparing an identifier for said add-in card with an identifier for said integrated component; and
if said identifiers match, writing information into the configuration spaces of the integrated component and add-in card.

2 (Previously Presented). The method of claim 1 further including enabling the add-in card to filter incoming wake packets, to determine whether it is necessary to awake a host platform and to provide an appropriate signal to wake the host platform.

3 (Previously Presented). The method of claim 1 further including coupling the add-in card to a platform.

4 (Previously Presented). The method of claim 3 further including enabling the platform to implement wireless medium access control.

Claim 5 (Canceled).

6 (Previously Presented). The method of claim 3 further including providing a physical layer for wireless communications in said add-in card.

Claims 7-11 (Canceled).

12 (Previously Presented). A processor-based system comprising:
a processor;
a medium access control to enable wireless communications, said medium access control not including a wake packet filtering function; and
a mating manager to access a configuration space associated with an integrated component, detect an add-in card external to said system, said add-in card intended to operate with an integrated medium access control to provide wake packet filtering, compare an identifier for said add-in card with an identifier for said medium access control, and if said identifiers match, write information into the configuration spaces of the integrated component and add-in card.

Claims 13-16 (Canceled).

17 (Currently Amended). An article comprising a ~~medium storing~~ computer readable medium encoded with computer executable instructions that enable enabling a processor-based system to:

access a configuration space on a platform integrated component;
detect an add-in card for a platform, said add-in card intended to operate with said integrated component to provide wake packet filtering;
compare an identifier for said add-in card with an identifier for said integrated component; and
if said identifiers match, write information into the configuration spaces of the integrated component and add-in card.

18 (Currently Amended). The article of claim 17 ~~further storing instructions that enable~~ wherein the computer readable medium is further encoded with computer executable instructions that enable the processor-based system to provide a physical layer for wireless communications.

19 (Currently Amended). The article of claim 17 ~~further storing instructions that enable~~ wherein the computer readable medium is further encoded with computer executable instructions that enable said system to avoid unnecessarily awakening a platform coupled to said system so as to reduce power consumption of said platform.

20 (Currently Amended). The article of claim 17 ~~further storing instructions~~ wherein the computer readable medium is further encoded with computer executable instructions that enable said system to implement wake packet filtering in a system not having a medium access control.

Claims 21-23 (Canceled).

24 (Currently Amended). The system of claim 12 ~~including an~~ wherein the add-in card coupled to said system, said add-in card including a physical layer to provide wireless communications, said add-in card also including a wake packet filtering function.